

15. (Currently Amended) A device for selecting a function to be implemented at a terminal, the device comprising:

a processor;

a first function path for implementing a first function; and

at least another function path for implementing at least another function;

wherein the first function path and the at least another function path run over at least two other different terminals of the processor, the first function path and the at least another function path being connected and being routed directly to the terminal, wherein:

the first function is selected for implementation by using a predefinable code for providing an implemented function, and

the predefinable code includes information pertaining to a waiting time corresponding to a period of time after which a switch is made from the first function to be implemented to the other function, the other function being suppressed during the period of time.

16. (Previously Presented) The device according to claim 15, further comprising an arrangement for selecting and clearing a function path of a function to be implemented independently of the predefinable code and for suppressing the at least another function path.

17. (Previously Presented) The device according to claim 15, wherein the first function includes a signal output unidirectionally over the first function path.

18. (Previously Presented) The device according to claim 15, wherein the at least another function includes communicating bidirectionally with a computer unit over the at least another function path.